

PACKAGE 3 COPIES OF SOME DATAMAX UV-1R SCHEMATICS

THESE SCHEMATICS WERE USED TO COME UP WITH THE ASTROCADE SYSTEM UPGRADE SHOWN IN PACKAGE 1. COMPARISON OF THE SCHEMATICS WILL SHOW THE SIMILARITIES OF THE ASTROCADE UPGRADE WITH THE DATAMAX UV-1R. FOR A COMPLETE SET OF DATAMAX UV-1R SCHEMATICS AND LAYOUTS (EXCLUDING POWER SUPPLY)

CONTACT:

DATAMAX INC.
1965 PRATT BLVD.
ELK GROVE VILLAGE, IL. 60007
(312) 981-8288

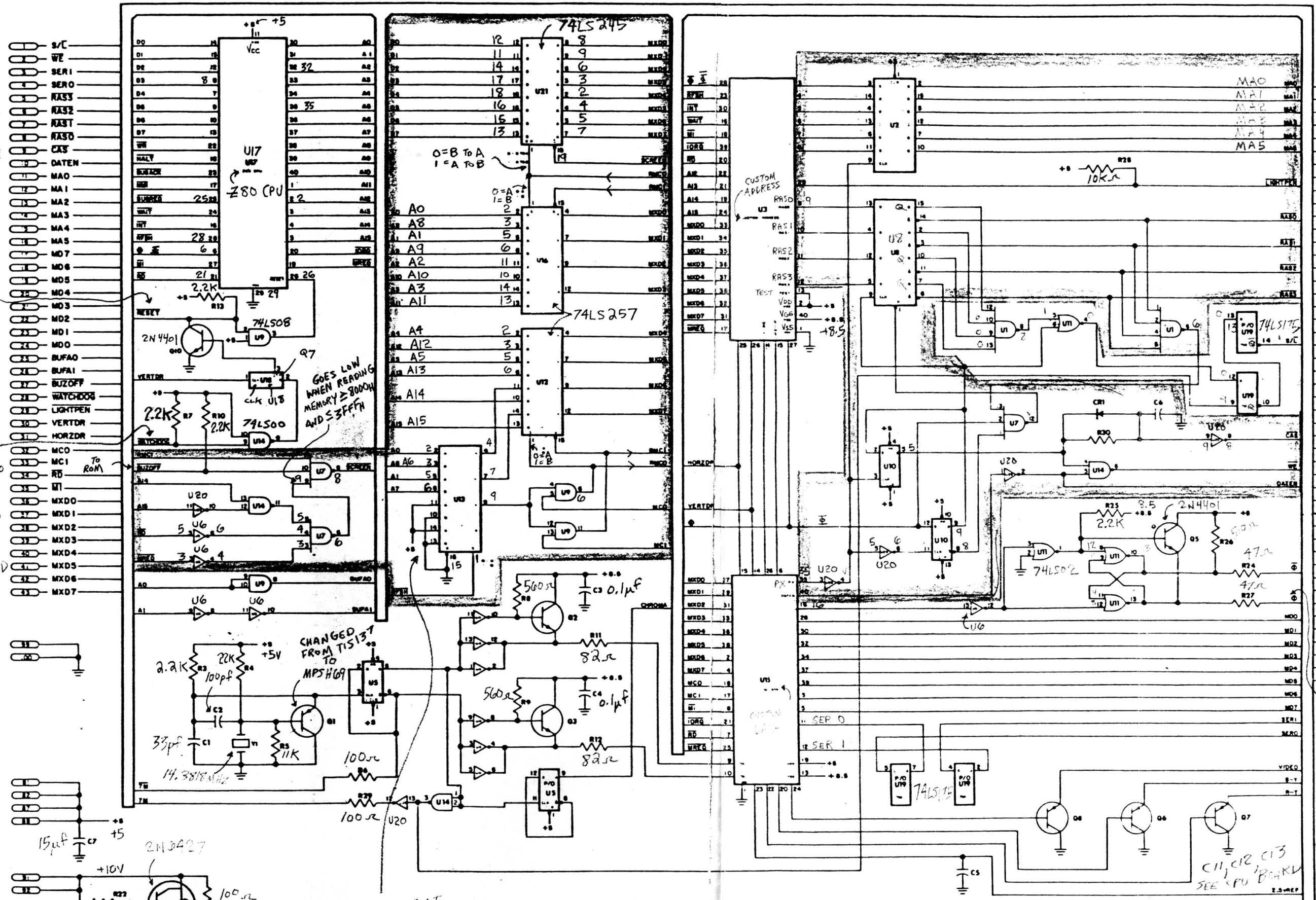
I PAID \$30 FOR ONE SET OF PRINTS (21 DRAWINGS). THE ABOVE ADDRESS IS OF JULY '84. IF DATAMAX HAS MOVED AGAIN, THE COMPANY, IF STILL OPERATING, WOULD PROBABLY BE LISTED IN THE CLASSIFIED DIRECTORY OF ILLINOIS MANUFACTURERS, WHICH MY DOWNTOWN LIBRARY WOULD HAVE A COPY OF. WRITE ME IF YOU CAN'T LOCATE DATAMAX.

THE NOTES ON THE ATTACHED DATAMAX SCHEMATICS WERE ADDED BY MYSELF WAY BACK WHEN I WAS FIRST STUDYING THE SCHEMATICS.

M. M.

TO RESET CIRCUIT. SEE LOGIC BOARD SCHEMATIC.

SEE LOGIC BOARD SCHEMATIC. WHEN WATCHDOG BIT IS PULLED HIGH, Q18 THEN RESTS TO GROUND AND SO RESETS.

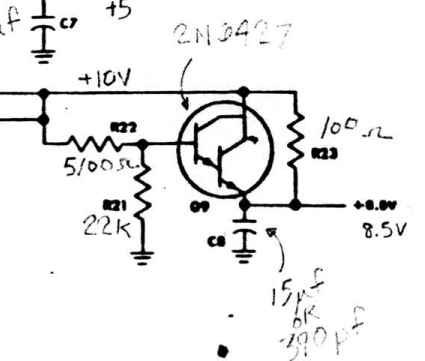


DO	50
D1	51
D2	
D3	
D4	
D5	
D6	
D7	
SCREEN	
RMCO	
RMCI	
A0	55
A1	56
A2	57
A3	58
A4	59
A5	60
A6	61
A7	62
A8	63
A9	64
A10	65
A11	66
A12	67
A13	68
A14	69
A15	70
RFSH	
WAIT	
BUSACK	
WR	
BUSREQ	
NMI	
IORQ	
MREQ	
HALT	
INT	
R-Y	
B-Y	
VIDEO	
2.5vREF	
	89
	90
CHROMA	
RESET	
7M	
7M	

LOADS
CUSTOM ADDRESS CHIP, CPU BOARD
74LS175, 74LS174, 74LS173, 74LS172, 74LS171, 74LS170, 74LS169, 74LS168, 74LS167, 74LS166, 74LS165, 74LS164, 74LS163, 74LS162, 74LS161, 74LS160, 74LS159, 74LS158, 74LS157, 74LS156, 74LS155, 74LS154, 74LS153, 74LS152, 74LS151, 74LS150, 74LS149, 74LS148, 74LS147, 74LS146, 74LS145, 74LS144, 74LS143, 74LS142, 74LS141, 74LS140, 74LS139, 74LS138, 74LS137, 74LS136, 74LS135, 74LS134, 74LS133, 74LS132, 74LS131, 74LS130, 74LS129, 74LS128, 74LS127, 74LS126, 74LS125, 74LS124, 74LS123, 74LS122, 74LS121, 74LS120, 74LS119, 74LS118, 74LS117, 74LS116, 74LS115, 74LS114, 74LS113, 74LS112, 74LS111, 74LS110, 74LS109, 74LS108, 74LS107, 74LS106, 74LS105, 74LS104, 74LS103, 74LS102, 74LS101, 74LS100, 74LS99, 74LS98, 74LS97, 74LS96, 74LS95, 74LS94, 74LS93, 74LS92, 74LS91, 74LS90, 74LS89, 74LS88, 74LS87, 74LS86, 74LS85, 74LS84, 74LS83, 74LS82, 74LS81, 74LS80, 74LS79, 74LS78, 74LS77, 74LS76, 74LS75, 74LS74, 74LS73, 74LS72, 74LS71, 74LS70, 74LS69, 74LS68, 74LS67, 74LS66, 74LS65, 74LS64, 74LS63, 74LS62, 74LS61, 74LS60, 74LS59, 74LS58, 74LS57, 74LS56, 74LS55, 74LS54, 74LS53, 74LS52, 74LS51, 74LS50, 74LS49, 74LS48, 74LS47, 74LS46, 74LS45, 74LS44, 74LS43, 74LS42, 74LS41, 74LS40, 74LS39, 74LS38, 74LS37, 74LS36, 74LS35, 74LS34, 74LS33, 74LS32, 74LS31, 74LS30, 74LS29, 74LS28, 74LS27, 74LS26, 74LS25, 74LS24, 74LS23, 74LS22, 74LS21, 74LS20, 74LS19, 74LS18, 74LS17, 74LS16, 74LS15, 74LS14, 74LS13, 74LS12, 74LS11, 74LS10, 74LS9, 74LS8, 74LS7, 74LS6, 74LS5, 74LS4, 74LS3, 74LS2, 74LS1, 74LS0

IT SEEMS THAT MICROCYCLER WILL ONLY WORK PROPERLY IF PINS 11, 14 GO TO GROUND AND PINS 10, 13 GO TO +5V. (THIS WAS CONFIRMED BY MODIFYING BALLY MOTHER BOARD)

28 0.1mfd CAPACITORS (BP) NOT SHOWN



C11, C12, C13 SEE CPU BOARD

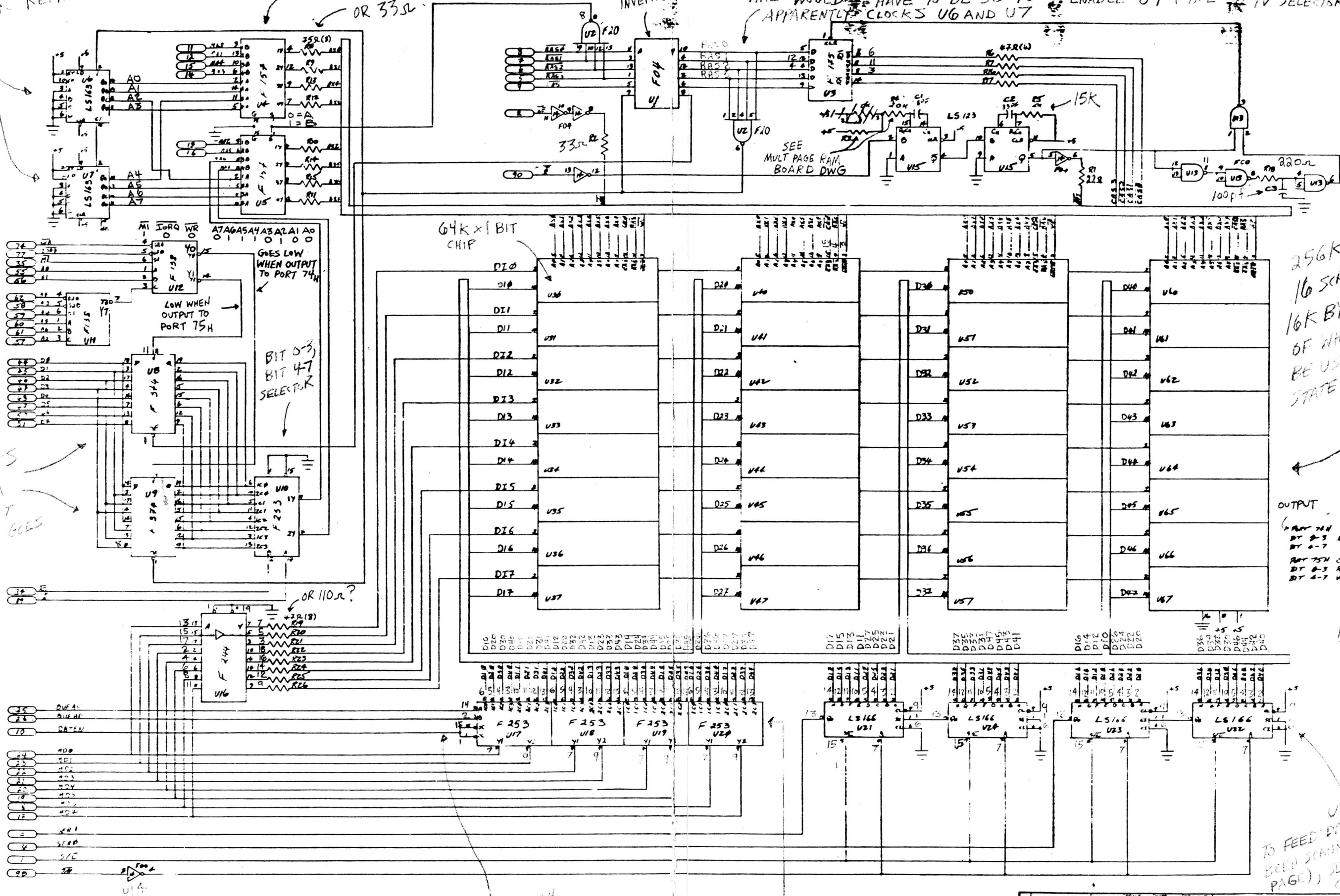
U6, U7 COUNTS
USED FOR REFRESHER

U4, U5 SELECTS
DATA A OR DATA B
OR 33Ω?

SCAN ADDRESS GENERATOR OF CUSTOM ADDRESS CHIP GOES FROM 0-FFFH
EVERY FRAME (1/60 SEC). APPARENTLY ALL FOUR RAS MUST GO HIGH SIMULTANEOUSLY.
THIS WOULD HAVE TO BE 56 TO ENABLE U9 (THE TV SELECTOR). THIS
APPARENTLY CLOCKS U6 AND U7

U11, U12
ARE PORT
74H, 75H
DECODERS.

U8, U9
ATCH DATA
OUTPUT TO PORTS
74H, 75H. DATA
REMAINS IN PORT
EVEN IF PINT GOES
HIGH.



256K SCREEN RAM.
16 SCREEN PAGES OF
16K BYTES EACH, 12
OF WHICH MAY ALSO
BE USED FOR SOLID
STATE DISK CACHE.

REFERENCE
P. 38 OF
OPERATOR
MANUAL

OUTPUT
PORT 74H TV SELECT
BT 0-3 READ (1 OF 16 PAGES)
BT 4-7 --
PORT 75H COMPUTER
BT 0-3 READ
BT 4-7 WRITE

NOTE

USED APPARENTLY
TO FEED DATA THAT HAS JUST
BEEN SCANNED (SEE TOP OF THIS
PAGE) SERIALLY INTO DATA
CHIP SETTING UP
THE VIDEO
DISPLAY.

GOES HIGH
WHEN DATA IS
TO BE WRITTEN.
(DISABLES F253)

DATA OUT
BUFFER

SCALE	DATE	DRAWN BY
TITLE: MULTIPLE PAGE SCREEN RAM		
FOR: COMMERCIAL CARD RACK		
NO.:	REV.:	DATE:
NO. 1152-02 1002-91409-A000		

