PACKAGE 4 44K BYTE DYNAMIC RAM ADD-ON

SCHEMATICS

USE THE FOLLOWING 3 SCHEMATICS IN PACKAGE 1 ASTROCADE SYSTEM UPGRADE AND MAKE THE DELETIONS SHOWN BELOW.

RAM BOARD 50 CONTACT EXPAND INTERFACE SCHEMATIC

USER RAM DECODER SCHEMATIC

USER RAM SCHEMATIC

DELETIONS

() RAM BOARD 50 CONTACT EXPAND INTERFACE SCHEMATIC.

DELETE LINES WR, M1, IORQ AND PX.

@ RAM BOARD USER RAM DECODER SCHEMATIC.

DELETE IC24 AND IC7. PIN 12 OF ICG SHOULD BE WIRED TO MREQ.

IC77 IS REQUIRED FOR FURTHER EXPANSION AND IS THEREFORE

OPTIONAL. IF IC77 IS DELETED, WIRE PIN 12 OF IC5 TO BUZOFF.

COMMENTS

ADD-ON REQUIRES ONLY +5V POWER SUPPLY. REFER TO POWER SUPPLY SCHEMATIC IN PACKAGE 1. SOLDER A O.I.M.F., 12V DISC CAPACITOR, FROM VCC TO GND, ON TO THE BOTTOM OF EACH WIREWRAP IC SOCKET. WIRE A 15 M.F. TANTALUM (OBSERVING POLARITY) AND O.I.M.F. DISC CAPACITOR, FROM +5V TO GND, WHERE THE +5V AND GND POWER LINES ENTER THE RAM BOARD. TWO LOW RESOLUTION DEMOS ARE AVAILABLE, FOR TESTING PURPOSES, AS DISCUSSED IN COMMENTS SECTION ON PACKAGE 2 TITLE PAGE. DITTO FOR ANY QUESTIONS REGARDING THE ADD-ON. REFER ALSO TO NOTES 12, 13 AND 14 IN PACKAGE 2. THIS ADD-ON IS RECOMMENDED ONLY FOR USE WITH SEPARATE COMPOSITE VIDEO AND AUDIO OUTPUTS FED TO A MONITOR TV. WIRE SPARES AS SHOWN BELOW.

